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854063.740APPLICATION NO.  
10/757,772INFORMATION DISCLOSURE STATEMENT  
(Use several sheets if necessary)

APPLICANTS

Francesco Pappalardo et al.

FILING DATE

January 14, 2004

GROUP ART UNIT  
2661

2609

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/HH/	AA	4,667,337	05/19/87	Fletcher	377	41	
/HH/	AB	2002/0156953	10/24/02	Beiley et al.	710	105	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
/HH/	AK	9-69075	11/03/97	JP (+ Abstract in English)		
/HH/	AL	0 886 220 A2	12/23/98	EP		
	AM					
	AN					
	AO					

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/HH/	AP	Pappalardo, F. et al., "Bus Switching Architecture," <i>STMicroelectronics</i> .
/HH/	AQ	Abstract of European Patent Application No. 03425013.4 filed January 14, 2003.
	AR	

EXAMINER

/Hooman Houshmand/

DATE CONSIDERED

06/25/2007

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).